

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	5546	((controller interface adapter)near4(board pcb card))same(chassis rack enclosure)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 14:16
L2	376031	(storage disk memory raid jbod)near3(board pcb card module array)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 14:18
L3	1203	1 same 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 14:19
L4	6192	(\$4plane motherboard)same((rout\$3 interface riser adapter passive\$)near2(pcb board card))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 14:40
L5	163	3 and 4 and(@ad<"20030722" @prad<"20030722" @rlad<"20030722")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 15:12
L6	352	4 same(horizont\$4 vertical\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 15:11
L7	288	(6 and(@ad<"20030722" @prad<"20030722" @rlad<"20030722"))not 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 15:43
L8	36	1 and 7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 15:18
L9	99	(2 and 7)not 8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 15:26

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L10	627	(4 and(361/6\$2 361/7\$2).ccls.)not(7 8 9)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 15:38
L11	26	6 and 10	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 15:42
L12	253	(2 and 10 and(@ad<"20030722" @prad<"20030722" @rlad<"20030722"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 15:43
S1	143185	(storage disk memory)near3 array	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 10:09
S2	338529	(\$5board \$5plane pcb card back\$5 motherboard)with(housing chassis enclosure rack)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/26 18:12
S3	309847	(storage disk memory)near3(card board pcb array)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/26 18:13
S4	7669	S2 same S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/26 18:14
S5	1335078	(power voltage current)near3(supply\$3 provid\$3 regulat\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/26 18:16
S6	2697	S4 and S5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/26 18:16
S7	280	S6 and("710"/\$3 "711"/\$3).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/26 19:28

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S8	232	S7 and(@ad<"20030722" @prad<"20030722" @rlad<"20030722")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 14:40
S9	2	"5604873".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/26 19:01
S10	1	"6363450".PN.	USPAT; USOCR	OR	ON	2007/06/26 19:02
S11	1	"6188571".PN.	USPAT; USOCR	OR	ON	2007/06/26 19:03
S12	1	"5995376".PN.	USPAT; USOCR	OR	ON	2007/06/26 19:04
S13	1	"5816673".PN.	USPAT; USOCR	OR	ON	2007/06/26 19:05
S14	1	"5309323".PN.	USPAT; USOCR	OR	ON	2007/06/27 14:11
S15	1	"5247427".PN.	USPAT; USOCR	OR	ON	2007/06/26 19:06
S16	533	(S6 and("361"/\$6).ccls.)not S8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/26 19:28
S17	438	S16 and(@ad<"20030722" @prad<"20030722" @rlad<"20030722")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/26 19:32
S18	82	S1 and S17	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/26 19:32
S19	5116	((storage disk memory)near3 array) raid jbod)with(board pcb card)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 14:17
S20	20067	(midplane adapter motherboard pcb)with((voltage current power)near4(regulat\$ suppl\$4 conver\$4 provid\$3 condition\$3 control\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 10:13

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S21	114	S19 and S20 and(@ad<"20030722" @prad<"20030722" @rlad<"20030722")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 10:14
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